Amendments to the Specification

Kindly amend the specification as follows:

Page 1, between the title and the heading "**BACKGROUND OF THE INVENTION**", insert

--CROSS REFERENCE TO RELATED APPLICATIONS

This is a divisional application of application Serial No. 10/073,022, filed March 1, 2004, which is hereby incorporated by reference in its entirety for all purposes.—

Please replace the paragraph beginning on page 5, line 20 with the following amended paragraph:

In this level shift circuit, the output of the inverter 1215 is at high level when the input signal IN is at low level. Therefore the nMOS transistor 1213 is OFF, and the nMOS transistor 1214 is ON. Since the nMOS transistor 1214 is ON, the potential of the node N2, that is, the signal level of the output signal OUT, is at low level. As a result, the pMOS transistor 1211 is ON, therefore the potential of the node [[N2]] N1 is at high level. This means that the pMOS transistor 1212 is OFF.

Please amend the abstract as follows:

A level shift circuit ~~whereby a voltage shift amount is large, operation speed is fast, and the power consumption is low. A p-type~~ including a first transistor [[is]] circuit connected between [[the]] a power supply line and [[the]] a first node, a ~~p-type~~ second

transistor [[is]] circuit connected between the power supply line and [[the]] a second node, ~~and an n-type third~~ a first transistor [[is]] connected between the ground line and the first node, and ~~an n-type fourth~~ a second transistor [[is]] connected between the ground line and the second node. [[The]] A gate of the first transistor circuit is connected to the second node, and [[the]] a gate of the second transistor circuit is connected to the first node. An input signal is supplied to [[the]] a gate of the [[third]] first transistor and an inverted value of the input signal is supplied to [[the]] a gate of the second ~~fourth~~ transistor. Additionally, ~~this level shift circuit has a plurality of control transistors. The~~ control transistors switch ~~transistor switches the~~ a ratio of [[the]] inflow current and emission current of the first node or the second node according to [[the]] a control signal. ~~The operation speed increases if this ratio is set high, and the voltage shift amount increases if this ratio is set low~~[[.]]